

**IN THE ABSTRACT:**

Please replace the Abstract of the Disclosure originally filed with the above-identified patent application with the following amended Abstract of the Disclosure:

In a drive unit for use with a matrix type display, a buffer circuit (B1) of the power supply circuit of the drive unit has an high-voltage first output circuit (B1p) and a low-voltage second output circuit (B1n) for generating the same output voltage (V1) under a normal operating condition, wherein the high-voltage output circuit (B1p) has enhanced capability of providing output current to bring up the output voltage (V1) and the low-voltage output circuit (B1n) has enhanced capability of providing output current to bring down the output voltage (V1). A voltage (~~detection voltage Vdet14~~) detected at a node connected to the output end of the buffer circuit (B1) is compared with a bias voltage (V1r). Based on the comparison, the outputs of the first and second output circuits are switched over from one to the other as it is supplied to the display. ~~This facilitates reduction of crosstalks in the display and improvement of the picture quality of the display.~~